Docket No.: S4-03P04584

O I A pereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450, on the date indicated below.

Date: February 28, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

10/566,529

Confirmation No.: N/A

Applicant Filed

Stephan Bolz, et al. January 30, 2006

Title

Device for Protecting Electronic Modules in a Multi-Voltage On-

Board Electrical System Against Short Circuits

Art Unit

N/A

Examiner Docket No.

to be assigned S4-03P04584

Customer No.:

24131

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are cited herewith:

Motorola Inc.: "FET Applications Manual", Phoenix, Arizona, USA, First Edition 1991-DL414/D, pgs. 55-57 and 267-268.

Respectfully submitted,

For Applicants .

Alfred K. Dassler 52,794

Date: February 28, 2006

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APASENT A	ND TRADEN	MARK OFFICE	Applicant Stephan Bolz, et al.						
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Filing Date January 30, 2006		Group Art Unit N/A		
		U.S. I	PATENT	DOCUMENTS					
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	0	Motorola Inc.: "FET Applications Manual", Phoenix, Arizona, USA, First Edition 1991-DL414/D, pgs. 55-57 and 267-268.							
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